

PALM INTRANET

Day: Monday Date: 11/28/2005 Time: 13:55:08

Inventor Name Search Result

Your Search was:

Last Name = CHIU First Name = KENNY

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09518489</u>	Not Issued	161		Microcontroller including a CPU having edge-sensitive non- maskable interrupt (NMI) circuitry and an interrupt control unit providing for NMI sharing	CHIU, KENNY KOK-HOONG
09566205	6401154	150		FLEXIBLE ARCHITECTURE FOR AN EMBEDDED INTERRUPT CONTROLLER	CHIU, KENNY KOK-HOONG
09760560	Not Issued	80	01/16/2001	Method and interface for glitch-free clock switching	CHIU, KENNY KOK-HOONG
09088788	6327259	150		FLEXIBLE PLACEMENT OF SERIAL DATA WITHIN A TIME DIVISIONED MULTIPLEXED FRAME THROUGH PROGRAMMABLE TIME SLOT START AND STOP BIT POSITIONS	CHIU, KENNY KOK-HOONG
09448249	6587954	150	11/24/1999	METHOD AND INTERFACE FOR CLOCK SWITCHING	CHIU, KENNY KOK-HOONG

Inventor Search Completed: No Records to Display.

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